

ABSTRACT OF THE DISCLOSURE

An information processor is provided with: two chip sets that are each provided with a serial controller, these chip sets being the interface
5 between a CPU and an input/output device; a selector for switching the connection routes between the serial controllers and the input/output device when a fault occurs in one of the chip sets; and a service processor for causing output data of the serial
10 controller of the working chip set to be supplied to the output device and data from the input device to be supplied to each of the two serial controllers when a fault has not occurred in the chip sets, and when a fault occurs in either one of the chip sets,
15 causing only the output data of the serial controller of the normal system to be supplied as output to the output device and data from the input device to be supplied to only the serial controller of the normal system.